

WEST Search History

DATE: Monday, July 19, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
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DB=JPAB; PLUR=YES; OP=ADJ

<input type="checkbox"/>	L17	reset and RTL	1
<input type="checkbox"/>	L16	L15 and reset	0
<input type="checkbox"/>	L15	memory and RTL	4

DB=PGPB,USPT; PLUR=YES; OP=ADJ

<input type="checkbox"/>	L14	L9 and reset capability	1
<input type="checkbox"/>	L13	L12 and reset capability	1
<input type="checkbox"/>	L12	L11 and circuit	210
<input type="checkbox"/>	L11	L10 and (level or design)	213
<input type="checkbox"/>	L10	L9 and cell	213
<input type="checkbox"/>	L9	L8 and RTL	356
<input type="checkbox"/>	L8	L7 and multiplex\$	41251
<input type="checkbox"/>	L7	memory and reset\$	146876
<input type="checkbox"/>	L6	mL5	112
<input type="checkbox"/>	L5	meory and reset\$	68
<input type="checkbox"/>	L4	resetable memory	5
<input type="checkbox"/>	L3	L2 and RTL	0
<input type="checkbox"/>	L2	resetable memory	5
<input type="checkbox"/>	L1	resetable mmeory	0

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER

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Hide?	Set Name	Query	Hit Count
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L11	l2 and resettable	14
<input type="checkbox"/>	L10	L9	43
<input type="checkbox"/>	L9	L7 and level and description	43
<input type="checkbox"/>	L8	L7 and level adn description	0
<input type="checkbox"/>	L7	L6 and circuit	43
<input type="checkbox"/>	L6	L5 and design	43
<input type="checkbox"/>	L5	L3 and reset\$	44
<input type="checkbox"/>	L4	L3 and rset\$	0
<input type="checkbox"/>	L3	L2 and infer\$	109
<input type="checkbox"/>	L2	L1 and RTL	910
<input type="checkbox"/>	L1	memory	458737

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER